

Science and Engineering Research Board
(a statutory body of the Department of Science & Technology, Government of India)

5 & 5A, LGF,
Vasant Square Mall
Sector-B, Pocket-5, Vasant Kunj,
New Delhi – 110 070

Brief report of the organized event
(Financial Assistance to Seminar / Symposia)

Date: 24.11.2022

SERB Sanction / File No: SSY/2022/ 000512

- 1. Name of Academic Institution / University / Society etc. under whose auspices the Seminar / Conference / Workshop / Symposium etc. was organized:**

KL University

- 2. Title of the Seminar / Conference / Workshop / Symposium etc.: Machine Learning and Deep Learning Techniques for VLSI**

- 3. Duration / Period of the organized event: 24.10.2022 to 28.11.2022**

- 4. Summary of the event (Max. 1000 Words):**

The inauguration was held on 24th Oct 2022 online at 9:30 am. Chief Guest of the program was Dr A.Srinadh, Dean Skill Development, KLEF, Dr. Dr. T.K.Rama Krishna Rao, Principal, KL, Principal, College of Engineering, KLEF and Dr.M.Suman, HOD,ECE,KLEF were also present with the participants.

Dean Skill Development addressed the participants about online FDP and various initiatives by SERB. And Dr.T.K Ramakrishna Rao ,Principal, College of Engineering,KLEF Given Responsibility in technical education and the importance of Workshop, Coordinator proposed the Vote of Thanks.

Outcome of FDP: Participants can be able to:

- 1) Solve practical and state of the art Machine Learning (ML) & Deep Learning (DL) problems to serve VLSI & MEMS industries.
- 2) Apply ML & DL design approaches to solve energy efficient VLSI Circuits and devices
- 3) Develop ML & DL algorithms compatible with Advanced VLSI

Some screenshots of the inaugural function are as below.

Photographs Section: Please paste high resolution photographs in given spaces below or may be submitted directly on online / email in JPEG format.

1. Inaugural Session



WELCOME To

SERB Sponsored National Workshop

On

Machine Learning and Deep Learning Techniques for VLSI

(24th to 28th Oct, 2022)

Organized by

Microelectronics Research Group (MERG)

Department of Electronics & Communication Engineering
Koneru Lakshmaiah Education Foundation (KL Deemed University), Vaddeswaram
Andhra Pradesh, India.



**Electronics &
Communication
Engineering**



SERB Sponsored Faculty Development Program
On

**Machine Learning and Deep Learning
Techniques for VLSI**

**Organized
by**

Microelectronics Research Group (MERG)
Department of Electronics & Communication Engineering
(DST - FIST Sponsored Department)
Koneru Lakshmaiah Education Foundation

24th to 28th Oct, 2022

Dr. Balawindar Raj, NITTTR
Lucknow



Topic: AI Applications in VLSI
Memories Design



Electronics &
Communication
Engineering



SERB Sponsored Faculty Development Program
On

Machine Learning and Deep Learning Techniques for VLSI

Organized
by

Microelectronics Research Group (MERG)
Department of Electronics & Communication Engineering
(DST - FIST Sponsored Department)
Koneru Lakshmaiah Education Foundation

24th to 28th Oct, 2022

Visiting Research Fellow
Cyber Security Research
Center, NTU, Singapore



Topic: Reconfigurable Computation
with machine learning methods



Electronics &
Communication
Engineering



SERB Sponsored Faculty Development Program
On

Machine Learning and Deep Learning Techniques for VLSI

Organized
by

Microelectronics Research Group (MERG)
Department of Electronics & Communication Engineering
(DST - FIST Sponsored Department)
Koneru Lakshmaiah Education Foundation

24th to 28th Oct, 2022

Dr. Sangeeta Singh
NIT, Patna



Topic: AI/ML Algorithms and
Applications in VLSI Design and
Technology



Electronics &
Communication
Engineering



SERB Sponsored Faculty Development Program
On
**Machine Learning and Deep Learning
Techniques for VLSI**

24th to 28th Oct, 2022

Organized
by

Microelectronics Research Group (MERG)
Department of Electronics & Communication Engineering
(DST - FIST Sponsored Department)
Koneru Lakshmaiah Education Foundation



Dr. Vadthiya Narendar
NIT, Warangal



Topic: Recent Nanoelectronic
Devices for Future Applications



Electronics &
Communication
Engineering



SERB Sponsored Faculty Development Program
On
**Machine Learning and Deep Learning
Techniques for VLSI**

24th to 28th Oct, 2022

Organized
by

Microelectronics Research Group (MERG)
Department of Electronics & Communication Engineering
(DST - FIST Sponsored Department)
Koneru Lakshmaiah Education Foundation



Dr. Ramesh Vaddi
SRM, University, AP



Topic: Circuits and Architecture
energy efficiency Approximation
computing techniques



Electronics &
Communication
Engineering



SERB Sponsored Faculty Development Program
On
**Machine Learning and Deep Learning
Techniques for VLSI**

24th to 28th Oct, 2022

Organized
by

Microelectronics Research Group (MERG)
Department of Electronics & Communication Engineering
(DST - FIST Sponsored Department)
Koneru Lakshmaiah Education Foundation



Dr.Kavicharan.M
NIT,Silchar



Topic: Machine Learning based
modelling and optimization of VLSI
interconnects and power
distribution networks



Electronics &
Communication
Engineering



SERB Sponsored Faculty Development Program
On
**Machine Learning and Deep Learning
Techniques for VLSI**

24th to 28th Oct, 2022

Organized
by

Microelectronics Research Group (MERG)
Department of Electronics & Communication Engineering
(DST - FIST Sponsored Department)
Koneru Lakshmaiah Education Foundation



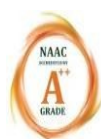
Dr.Reshmi maity
Mizoram University



Topic: MEMS/NEMS Micromachined
Ultrasonic Transducers(FEM CAD
Modeling)



Electronics &
Communication
Engineering



SERB Sponsored Faculty Development Program
On
**Machine Learning and Deep Learning
Techniques for VLSI**

24th to 28th Oct, 2022

Organized
by

Microelectronics Research Group (MERG)
Department of Electronics & Communication Engineering
(DST - FIST Sponsored Department)
Koneru Lakshmaiah Education Foundation



Dr.Sangeeta Nakhate
MANIT,Bhopal



Topic: Design of Machine Learning
Resistant Physical Unclonable
Functions

(Convener Signature)